A Novel Approach of CTL Model Checking Based on Probe Machine

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Abstract—Model checking has established as an effective method for automatic system analysis and verification. It is making its way into many domains and methodologies. However, the state space may be extremely large for many practical systems, and this is a major limitation for state-space search algorithms in model checking. We have proposed a novel computing model called probe machine in 2016, which is a fully parallel computing model. In comparison to the Turing machine, it can solve the graph search problems efficiently, which can overcome the existing model checking limitations. In this paper, we propose a novel approach to perform Computation Tree Logic (CTL) model checking based on the mathematical model of probe machine, which can verify all CTL properties. It can greatly reduce the verification time for systems with large state space. We develop a model checker called CTL2PROBE based on our approach and the experimental results show that our approach is better than NuSMV.

Index Terms—Model Checking, Probe Machine, Graph Search, Computation Tree Logic

I. INTRODUCTION

Model Checking is a technology that can verify whether a system satisfies the given property automatically [1]. The way of verification is exploring all possible system states in a brute-force manner [2]. In this way, it is a real challenge to examine the largest possible state spaces that can be treated with current means, i.e. processors and memories. Therefore, the chief limitation is the state explosion problem where the size of the global state graph grows exponentially with the size of the program itself [3].

In order to deal with the state explosion problem, many approaches have been proposed. Ordered Binary Decision Diagram (OBDD) is proposed by McMillan to represent state space, improving the scale of verified system [4], [5]. Another successful technique for dealing with state explosion is based on the partial order reduction [6], [7]. It exploits the independence of concurrently executed events. Although symbolic representations and partial order reduction have greatly increased the size of the systems that can be verified, many realistic systems are still too large to be handled. Thus, some researchers have turned their attention to other novel computation architectures like molecules and DNA to break through the limitations of Turing machines.

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In 2006, Turing Award winner Allen Emerson use DNA molecules to conduct CTL model checking for the first time [8]. In this work, he proposes a DNA-computing-based method and designs a checking algorithm for CTL formula $\mathbf{EF}p$ where Kripke structures are used to model the system and the states and transitions of the model are encoded into DNA strands. It permits a very compact representation of extremely large state graphs. For example, a graph of size 10^{16} states can be represented within 0.01 liter of DNA. Therefore, the parallelism of DNA computing and the vast storage of DNA molecules provide the opportunity to break the state space limitation on traditional electronic computers.

Probe machine is a mathematical model proposed by Xu in 2016 [9]. It can be implemented by using nano-DNA probe technologies. It is a fully parallel computing model in the sense that it can simultaneously process multiple pairs of data, rather than sequentially process every pair of linearly adjacent data. Many NP-complete problems, i.e., the graph coloring, Hamilton cycle problems, traveling salesman problem [10], and postman problem [11] have been solved based on the probe machine. Probe machine can enumerate all solutions to these problems by only one probe operation.

Similarly, the way of CTL model checking is exploring all possible system states in a brute-force manner and finding a path satisfying the given property. Therefore, we propose a novel method to perform CTL model checking based on the probe machine. Compared to traditional model checkers, our approach can relieve the state explosion problem and reduce the verification time.

In summary, this paper makes the following contributions:

- We design a mapping algorithm to transform the model of Kripke structure into the data library and probe library that can run directly on the probe machine.
- We develop a model checker called CTL2PROBE to simulate the probe machine, which takes the model as input and obtains all feasible paths or counterexamples.
- We conduct several experiments based on different numbers of states. Compared to NuSMV, the experiment results prove the feasibility and efficiency of our approach.

The rest of this paper is organized as follows. Section II briefly introduces CTL model checking and the concept of probe machine. Section III presents our approach and provides complexity analysis. Section IV introduces the model checker

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CTL2PROBE based on our approach, and the experimental results show that our approach is better than NuSMV. Section V concludes our work.

II. PRELIMINARIES

A. CTL Model Checking

A Kripke structure is a variation of the transition system, originally proposed by Saul Kripke [12], used in model checking [13] to represent the behavior of a system. It consists of a graph whose nodes represent the reachable states of the system and whose edges represent state transitions, together with a labeling function which maps each node to a set of properties that hold in the corresponding state.

A Kripke structure is defined as a four-tuple

$$\mathcal{M} = \{S, I, R, L\}$$

- a finite set of states S.
- a set of initial states $I \subseteq S$.
- a transition relation $R \subseteq S \times S$.
- a labeling function $L: S \to 2^{AP}$.

Computation Temporal Logics properties are traditionally interpreted in terms of Kripke structures. Clarke has proved that any CTL formula can be expressed in terms of \neg , \lor , **EX**, **EU** and **EG** [13]. Thus, it is sufficient to be able to handle six cases, depending on whether g is atomic or has one of the following forms: $\neg f_1$, $f_1 \lor f_2$, **EX** f_1 , **E**[f_1 **U** f_2] or **EG** f_1 . These CTL formulas describe the following properites.

$$\begin{split} \mathcal{M}, s &\models \neg f_1 &\Leftrightarrow \mathcal{M}, s \not\models f_1 \\ \mathcal{M}, s &\models f_1 \lor f_2 &\Leftrightarrow \mathcal{M}, s \models f_1 \text{ or } \mathcal{M}, s \models f_2 \\ \mathcal{M}, s &\models EXf_1 &\Leftrightarrow \text{ there exists a state } t \text{ such that } \\ \mathcal{R}(s,t) \text{ and } \mathcal{M}, t \models f_1 \\ \mathcal{M}, s &\models E[f_1 U f_2] \Leftrightarrow \text{ there exists an infinite path } \pi \\ \text{ starting at } s \text{ and there exists a } \\ k \geq 0 \text{ such that } \mathcal{M}, s_k \models f_2 \\ \text{ and for all } 0 \geq j < k, \mathcal{M}, s_j \models f_1 \\ \mathcal{M}, s \models EGf_1 &\Leftrightarrow \text{ there exists an infinite path } \pi \\ \text{ starting at } s \text{ such that for all } \\ i \geq 0, \mathcal{M}, s_i \models f_1 \end{split}$$

B. Probe Machine

Probe Machine (PM) is defined as a nine-tuple

$$PM = (X, Y, \sigma_1, \sigma_2, \tau, \lambda, \eta, Q, C)$$

where the nine components denote the data library (X), probe library (Y), data controller (σ_1) , probe controller (σ_2) , probe operation (τ) , computing platform (λ) , detector (η) , true solution storage (Q), and residue collector (C). The following will introduce four main components: data library, probe library, probe operation, and computing platform.

• The data library X is viewed as a set of n elements, denoted by $X = \{x_1, x_2, ..., x_n\}$. Each data x_i contains a body and p types of data fibers. Data x_i is defined as

$$x_i = \{x_i^1, x_i^2, \dots, x_i^p\}$$

- The probe in the probe machine is defined as a tool to find two data and implement some operations (e.g., connective and transitive operations) between them. Formally, let x_i^j and x_t^m be two types of data fibers. The probe between x_i^j and x_t^m denoted as $\tau^{x_i^j x_t^m}$. A connective probe, denoted as $\overline{x_i^j}, x_t^m$, refers to a probe $\tau^{x_i^j x_t^m}$ connecting two target data fibers x_i^j and x_t^m , forming a high-order aggregation.
- A probe operation τ is a process of executing many probe operations simultaneously.
- The computing platform, denoted by λ, is an environment to conduct probe operations τ. It helps probes rapidly find the target data fibers and then conduct probe operations. High cohesiveness, threshold property, and uniqueness are the fundamental functions of the computing platform.
 - High cohesiveness is the rule that high-order aggregation data are given higher priority than low-order aggregation to be executed probe operation.
 - Threshold property limit that the size of two aggregations for probe operation must not exceed the size of the graph itself.
 - Uniqueness is the rule that there is at most one data for each type that an aggregation contains.

III. MODEL CHECKING BASED ON PROBE MACHINE

This section is concerned with CTL model checking on the probe machine. We first introduce the procedures of model checking on the probe machine. Construction of the data library and probe library are two main steps. Subsequently, we propose the methods to construct data library and design methods for probe library of **EG** and **EU** respectively. In the end, we analyze the time complexity for the designed approach.

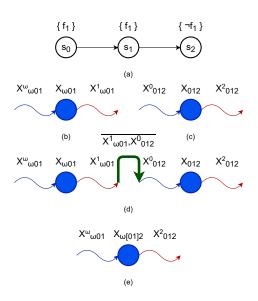
A. Procedures of Model Checking on Probe Machine

Xu solve the graph coloring problems with connective probes that connect two adjacent vertexes of different colors [9]. Inspired by the previous work, we propose a graph search mechanism for model checking. Here, we use a probe to connect two adjacent states in the graph which satisfying the CTL property. Each subpath is considered as data to be probed. In this way, each subpath continuously grows after each probe operation until the initial state is included in the path.

The procedures of model checking on the probe machine are divided into four steps as follows

- Construction of the data library X.
- Establishment of the probe library Y according to the CTL property.
- Implement the probe operation τ(X, Y) in the computing platform λ, producing a large number of solutions.
- Find the true paths T of all solutions.

Specially, Clarke has proved that any CTL formula can be expressed in terms of \neg , \lor , **EX**, **EU** and **EG** [13]. Thus, it is sufficient to be able to handle six cases. We usually check L(s) to verify $\mathcal{M}, s \models \neg f_1, \mathcal{M}, s \models f_1 \lor f_2$ and traverse the successors of the state s to check $\mathcal{M}, s \models \mathbf{EX}f_1$. These



S₀ ¬Start ¬Close ¬Heat start over S S, close doo start cooking cooł Start Start Start ¬Close Close Close -Heat -Heat Heat open doo done reset S_3 restar ¬Start Close -Heat

Fig. 2. Microwave oven example.

Fig. 1. (a) A simple Kripke Structure. (b) Data $x_{\omega 01}$ with its two data fibers $\frac{x_{\omega 01}^{\omega}}{x_{\omega 01}^{1}}$. (c) Data x_{012} with its two data fibers x_{012}^{0} , x_{012}^{2} . (d) Probe $x_{\omega 01}^{1}$, x_{012}^{0} (e) Two-aggregation data $x_{\omega [01]2}$.

three verifications can be solved within a short time, so we don't need some particular methods on the probe machine to check them. Therefore, only two algorithms are needed for **EG** and **EU**. And the following will respectively introduce how to build the data library and probe library for $\mathcal{M}, s \models \mathbf{E}Gf_1$ and $\mathcal{M}, s \models \mathbf{E}[f_1\mathbf{U}f_2]$ respectively.

B. Construction of Data Library

Let a graph G mean a Kripke structure, $\mathcal{M} = \{S, I, R, L\}$. We denote V(G) and E(G) as the sets of states and transitions of G respectively. G has a set of nodes $V(G) = \{v_1, \ldots, v_n\}$ and a set of edges $E = \{e_1, \ldots, e_p\}$, as well as $L(v_i) \in \{f_1, \neg f_1\}$ holds for each v_i .

In addition, we denote $Pre(v_i)$ and $Suc(v_i)$ as the sets of predecessor nodes and successor nodes of the vertex v_i . $E(v_i)$ is defined as the set of edges out of the vertex v_i . Let $E^2(v_i)$ be the set of all directed two-paths with internal vertex v_i . Formally

$$E^{2}(v_{i}) = \{v_{l}v_{i}v_{r} \triangleq x_{lij} | v_{l} \in Pre(v_{i}), v_{r} \in Suc(v_{i})\}$$
(1)

Data x_{lij} are defined according to (1). Let's take an example[see Fig. 1(a)], it is a simple Kripke structure with 3 states and 2 transitions. Every state is considered as data with its transiton edge, such as $E^2(v_1) = \{x_{012}\}$ [see Fig. 1(c)].

Based on $E^2(v_i)$, we construct the data library X of the connective probe machine as follows:

$$X = \bigcup_{i=1}^{n} E^{2}(v_{i}) =$$

=
$$\bigcup_{i=1}^{n} \{ x_{lir} \triangleq v_{l}v_{i}v_{r} | v_{l} \in Pre(v_{i}), v_{r} \in Suc(v_{i}) \}$$
(2)

where x_{lir} has exactly two types of data fibers, the left one is x_{lir}^{l} and the right one is x_{lir}^{r} . Typically, initial states have no predecessor and final states have no successor, so we define ω as empty. The data $x_{\omega 01}$ of the initial state s_0 is shown in Fig. 1(b).

In fact, each data represent a path and a *n*-aggregation data represents a path of length *n*. For example, data x_{012} represents a one-length path, $\pi = v_1$ [see Fig. 1(c)], and 0 and 2 respectively represent the possible node connecting this path. Similarly, a two-aggregation data $x_{\omega[01]2}$ represents a two-length path, $\pi = v_0, v_1$ [see Fig. 1(e)].

C. Probe Library for EGf_1

The logical operator **EG** means that there is a path so that all future states on it are satisfied. Based on this property, we set a rule for the probe library Y of **EG** f_1 as follows.

Rule 1: Let x_{utv} and x_{lir} be two data in X. Then there exists a probe $\overline{x_{utv}^v}, x_{lir}^l$ between them if and only if l = t, v = i and $f_1 \in L(v_t)$, $f_1 \in L(v_i)$.

As shown in Fig. 1(d), $f_1 \in L(v_0)$, $f_1 \in L(v_1)$ and the data $x_{\omega 01}$ is adjacent to another data x_{012} , so there exists a probe $x_{\omega 01}^1$, x_{012}^0 to connect the two data fibers, which forming a two-aggregation data $x_{\omega [01]2}$ and remaining two data fiber $x_{\omega 01}^{\omega}$ and x_{012}^{2} [see Fig. 1(e)].

By taking the graph in Fig. 2 as an example, the following steps describe the process of checking $\mathcal{M}, s_0 \models \mathbf{EG} \neg Heat$ by our approach.

Step 1: Data library X can be constructed as follows:

$$X = E^{2}(v_{0}) \cup E^{2}(v_{1}) \cup E^{2}(v_{2}) \cup E^{2}(v_{3}) \cup E^{2}(v_{4}).$$

where, $E^2(v_0) = \{x_{\omega 01}\}, E^2(v_1) = \{x_{012}, x_{212}, x_{312}\}, E^2(v_2) = \{x_{121}, x_{123}, x_{124}, x_{421}, x_{423}, x_{424}\}, E^2(v_3) = \{x_{231}\}, \text{ and } E^2(v_4) = \{x_{242}, x_{244}, x_{442}, x_{444}\}.$ Let $\zeta(x)$ represent data fibers of data x. There are total 15 types of data in X, and 30 types of data fibers has been generated as follows:

$$\begin{split} \zeta(x_{\omega01}) &= \{x_{\omega01}^{\omega}, x_{\omega01}^{1}\}, \, \zeta(x_{012}) = \{x_{012}^{0}, x_{012}^{2}\} \\ \zeta(x_{212}) &= \{x_{212}^{2}, x_{212}^{2}\}, \, \zeta(x_{312}) = \{x_{312}^{3}, x_{312}^{2}\} \\ \zeta(x_{121}) &= \{x_{121}^{1}, x_{121}^{1}\}, \, \zeta(x_{123}) = \{x_{123}^{1}, x_{123}^{3}\} \\ \zeta(x_{124}) &= \{x_{124}^{1}, x_{124}^{4}\}, \, \zeta(x_{421}) = \{x_{421}^{4}, x_{421}^{4}\} \\ \zeta(x_{423}) &= \{x_{423}^{4}, x_{423}^{3}\}, \, \zeta(x_{424}) = \{x_{424}^{4}, x_{424}^{4}\} \\ \zeta(x_{231}) &= \{x_{221}^{2}, x_{231}^{2}\}, \, \zeta(x_{242}) = \{x_{242}^{2}, x_{242}^{2}\} \\ \zeta(x_{244}) &= \{x_{244}^{2}, x_{244}^{4}\}, \, \zeta(x_{442}) = \{x_{442}^{4}, x_{442}^{4}\} \\ \zeta(x_{444}) &= \{x_{444}^{4}, x_{444}^{4}\}, \end{split}$$

Step 2: Probe library construction

Based on the data library X, we construct the corresponding probe library Y according to rule 1.

$$\begin{split} Y_{01} &= \{x_{\omega01}^1, x_{012}^0\} \\ Y_{12} &= \{\overline{x_{012}^2, x_{121}^1}, \overline{x_{012}^2, x_{123}^1}, \overline{x_{012}^2, x_{124}^1}, \\ & \overline{x_{212}^2, x_{121}^1}, \overline{x_{212}^2, x_{123}^1}, \overline{x_{212}^2, x_{124}^1}, \\ & \overline{x_{312}^2, x_{121}^1}, \overline{x_{312}^2, x_{123}^1}, \overline{x_{312}^2, x_{124}^1}\} \\ Y_{21} &= \{\overline{x_{121}^1, x_{212}^2}, \overline{x_{421}^1, x_{212}^2}\} \\ Y_{23} &= \{\overline{x_{123}^3, x_{231}^2}, \overline{x_{423}^3, x_{231}^2}\} \\ Y_{31} &= \{\overline{x_{231}^1, x_{312}^3}\} \\ Y_{24} &= Y_{42} = \emptyset \end{split}$$

Step 3: Probe operations

In each iteration, probes connect the probable data to form larger data, namely, aggregations. After several rounds, each data containing the initial state is a feasible path satisfying the property, otherwise, the largest data is a counterexample.

Probe operation rules are based on three functions of the computing platform: high cohesiveness, threshold, and uniqueness. They ensure that aggregations grow quickly according to the rules. By taking the oven example, the process of probe operation is as follows.

Data library X and probe library Y have been constructed, and the following Table I showing how to obtain the feasible paths in the computing platform λ . For model checking, the number of iterations is at most equal to $\lceil log_2n \rceil + 1$, n is the number of vertexes in the graph G. In this example, it takes three steps for probe operations.

TABLE I SOLUTIONS PROCEDURES

	Computing Platform				
0	$x_{\omega 01}, x_{012}, x_{212}, x_{312} \dots$				
1	$x_{\omega[01]2}, x_{0[12]3}, x_{1[21]2}, x_{1[23]1}, x_{2[31]2} \dots$				
2	$x_{\omega[0123]1}, x_{0[123]1}, x_{\omega[012]3}, x_{4[231]1} \dots$				
3	$x_{\omega[0123]1}$				

In the first iteration, the probe $\overline{x_{\omega01}^1, x_{012}^0}$ find the data fiber $x_{\omega01}^1$ of the data $x_{\omega01}$ and x_{012}^0 of x_{012} , connecting

them and forming a two-aggregation data $x_{\omega[01]2}$. And the data $x_{1[24]4}$ isn't formed for lack of probe $\overline{x_{124}^4, x_{244}^2}$. Many two-aggregations are generated in this round, but some are not shown in the table I.

In the second iteration, some four-aggregations and threeaggregations are generated. For example, the probe $\overline{x_{012}^2, x_{123}^1}$ find the data fiber x_{012}^2 of the data $x_{\omega[01]2}$ and x_{123}^1 of $x_{1[23]1}$, connecting them and forming a four-aggregation data $x_{\omega[0123]1}$.

In the third iteration, no larger aggregation is produced, so the iteration stops.

It is clear that $x_{\omega[0123]1}$ contians the initial node s_0 and a cycle $\pi = s_1 s_2 s_3 s_1 s_2 s_3 \dots$, so it can be concluded that $\mathcal{M}, s_0 \models \mathbf{EG} \neg Heat$ and an available path is $\pi = s_0 s_1 s_2 s_3 s_1 s_2 s_3 \dots$

For the problem of $\mathbf{EG}f_1$, all true solutions are the aggregations including the initial state s_0 and a cycle, and feasible paths are recorded by aggregations themselves. Otherwise, it means $\mathcal{M}, s_0 \not\models f_1$. Furthermore, the largest aggregation can be the counterexample, since it represents the path closest to true solutions.

D. Probe Library for $E[f_1Uf_2]$

The logic operator **EU** means that there is a path keeping a state before another certain state appears. Based on this property, two rules are set as follows to establish probe library Y of $\mathbf{E}[f_1\mathbf{U}f_2]$.

Rule 1: Let x_{utv} and x_{lir} be two data in X. Then there exists a probe $\overline{x_{utv}^v}, x_{lir}^l$ between them if and only if l = t, v = i and $\{\neg f_2, f_1\} \in L(v_t), f_2 \in L(v_i)$.

Rule 2: Let x_{utv} and x_{lir} be two data in X. Then there exists a probe $\overline{x_{utv}^v}, x_{lir}^l$ between them if and only if l = t, v = i and $\{\neg f_2, f_1\} \in L(v_t), f_1 \in L(v_i)$.

Rule 1 and rule 2 ensure that only one state on the path satisfies f_2 and lies at the endpoint. By taking the graph in Fig. 2 as an example, the following steps describe the process of checking $\mathcal{M}, s_0 \models \mathbf{E}[\neg Heat \mathbf{U} \ Close]$ on probe machine.

Based on the two rules, we construct the corresponding probe library Y as follows.

$$\begin{split} Y_{01} &= \{\overline{x_{\omega 01}^{1}, x_{012}^{0}}\}\\ Y_{12} &= \{\overline{x_{012}^{2}, x_{121}^{1}}, \overline{x_{012}^{2}, x_{123}^{1}}, \overline{x_{012}^{2}, x_{124}^{1}}, \\ & \overline{x_{212}^{2}, x_{121}^{1}}, \overline{x_{212}^{2}, x_{123}^{1}}, \overline{x_{212}^{2}, x_{124}^{1}}, \\ & \overline{x_{312}^{2}, x_{121}^{1}}, \overline{x_{312}^{2}, x_{123}^{1}}, \overline{x_{312}^{2}, x_{124}^{1}}\}\\ Y_{21} &= Y_{23} = Y_{24} = Y_{31} = Y_{42} = \emptyset \end{split}$$

And the following Table II showing how probe operation τ is executed to obtain solutions in the computing platform λ . After three iterations, the aggregation $x_{\omega[012]3}$ and $x_{\omega[012]4}$ contian the initial node s_0 , it can be concluded that $\mathcal{M}, s_0 \models \mathbf{E}[\neg Heat \mathbf{U} \ Close]$ and an available path is $\pi = s_0 s_1 s_2$

E. Complexity Analysis

We analyze the time complexity in terms of three steps proposed in Section III-A.

TABLE II SOLUTIONS PROCEDURES

	Computing Platform			
0	$x_{\omega 01}, x_{012}, x_{212}, x_{312} \dots$			
1	$x_{\omega[01]2}, x_{0[12]3}, x_{0[12]4} \dots$			
2	$x_{\omega[012]3}, x_{\omega[012]4}, \dots$			
3	$x_{\omega[012]3}, x_{\omega[012]4}$			

Theorem 1: The time complexity of data library X is at most $O(V \times E^2)$.

For each vertex, outgoing and incoming edges need to be visited. Thus, the time complexity of constructing the data library X is $O(V \times E^2)$.

Theorem 2: The time complexity of probe library Y is at most $O(V \times E)$.

We construct the probe library Y by traversing every vertex with its predecessor nodes and its time complexity is $O(e_1)$, and e_1 is the number of its predecessor. The total time complexity of this process is at most $O(V \times E)$.

For model checking, the number of iterations is at most equal to $\lceil log_2n \rceil + 1$, *n* is the number of vertexes in the graph *G*. Thanks to the underlying parallelism of the probe machine, the processing ability of one probe operation τ is 2^q , *q* is the number of all possible edges to probe [9]. Therefore, our approach of CTL model checking based on the probe machine can greatly reduce the verification time for systems with large state space.

IV. PROTOTYPE TOOL

This section is concerned with the framework of our tool CTL2PROBE and simulation of CTL model checking on probe machine. First, we will introduce a model checker called CTL2PROBE based on our approach. And then, some simulations are conducted, which prove the feasibility and efficiency of our approach.

A. CTL2PROBE

To simulate automatic verification of the CTL model checking on the probe machine, we develop a model checker called CTL2PROBE. Fig. 3 shows the framework of CTL2PROBE. It consists of three functional modules: Parsing, Modeling, and Computing. The specific design is as follows:

Parsing: This module mainly parses a JSON file into a model and CTL formula. In this process, the transitions between states are recorded in a HashMap structure and the CTL formula is transformed into a parse tree. It takes a particular format JSON file as input. We define this special format to effectively simplify the parsing process. It is a structured markup language similar to XML that users can quickly obtain or change the contents of elements. As shown in Fig. 4, it is a JSON file that describing the state s_0 of oven example at Section III. The file includes the label, predecessor,

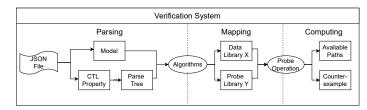


Fig. 3. The Framework Design of Verification System

```
"s0": {
    "lab": ["!Start", "!Close", "!Heat"],
    "pre": [],
    "suc": [1]
},
```

Fig. 4. The JSON file of the oven example

and successor of each state and provides the CTL formula for verification. In the module, CTL formula will be transformed into the formula in terms of \neg , \lor , **EX**, **EU** and **EG**, and stored in the data structure based on parse tree [see Fig. 5]. It is a bottom-up solving process and the given formula is satisfied when the top node contains the initial node.

Mapping: This module is responsible for mapping elements into the data library and probe library. The rule of mapping is the described in Section III. Data library and probe library are provided for the computing module to verify CTL property.

Computing: This module is used to implement probe operations according to the data library and probe library. The rule of computing is based on three functions of the computing platform: high cohesiveness, threshold, and uniqueness. We realize them according to the following rules.

- High cohesiveness: High-order aggregation data are given higher priority than low-order aggregation to be executed probe operation.
- Threshold: The size of two data for probe operation must not exceed the size of the graph itself.

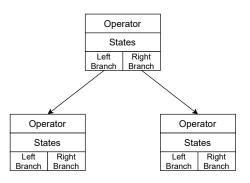


Fig. 5. The data structure of parse tree nodes

```
****** Data Library X ******
w01 012 212 <u>312</u> 121 123
124 421 423 424 231 242
244 442 444 01234 41234
****** Probe Library Y *******
<w01,012> <121,212> <421,212> <231,312> <012,121> <212,121>
<312,121> <012,123> <212,123> <312,123> <012,124> <212,124>
<<312,124> <123,231> <423,231>
*******
P 1: ['w012', 'w01234', '1212', '4212', '2312', '0121', '2121', '31
21', '0123', '2123', '0124', '2124', '3124', '1231', '4231']
P 2: ['w012', 'w0123', 'w0124', '23121', '23123', '23124', '12312',
'42312', '01231', 'w01231', '2123', '31231']
P 3: []
******* Result *******
Satisfying, and Path is: s0-s1-s2-s3, s0-s1-s2-s1
Modeling Time: 0.000128021240234375
Verification Time: 0.00031495094299316406
```

Fig. 6. The process of verification

• Uniqueness: There is at most one data for each type that an aggregation contains.

B. Simulation

To verify the effectiveness and efficiency of the proposed algorithm, we have carried out several experiments on CTL2PROBE.

As shown in the Fig. 6, it is the result of verification of $\mathbf{EG} \neg Heat$ in the oven example. It takes a JSON file as the input of the program and the output consists of four parts: data library, probe library, the process of verification, and result. The result part consists of all available paths, modeling time, and verification time. The total time of this verification is $1240 \times 10^{-6}s$, but NuSMV takes $15313 \times 10^{-6}s$ to verify the oven example. It is powerful proof of the efficiency of our tool.

To further prove the efficiency of verifying the model with more states, we have carried out several experiments. We randomly generate multiple graphs with 5, 10, 50 states. Each of the instances runs independently 10 consecutive times to measure the average runtime. The experiment is programmed by Python and executed on a computer system with specifications of Intel Core i-7 at 2.2 GHz CPU and 16 GB RAM under macOS operating system.

The experimental results are shown in Table III. The results show that CTL2PROBE is better than NuSMV in some cases. However, the verification time by CTL2PROBE will increase significantly as the number of nodes increases. Because CTL2PROBE is a tool to simulate probe machine for probe operation and the properties of huge capacity and total parallel are difficult to realize on CTL2PROBE. It is definite that model checking on the probe machine is much faster than traditional model checkers. Our experiment proves the feasibility and efficiency of model checking on the probe machine.

V. CONCLUSION

In this paper, a novel CTL model checking approach based on the probe machine is proposed, which can solve the

TABLE III COMPARISON OF CTL2PROBE WITH NUSMV ON SOME CASES

Nodes	C	NuSMV(10 ⁻⁶)			
Noues	Modeling	Computing	Total Time		
5	426	301	727	13291	
10	1127	957	2084	21532	
50	17402	1574	18976	30804	

limitation for state-space explosion and reduce the verification time for systems with large state space. We design a mapping algorithm to transform the model of Kripke structure into the data library and probe library that can run directly on the probe machine. We develop a model checker called CTL2PROBE to simulate the probe machine, which takes the model as input and obtains all feasible paths or counterexamples. Compared to NuSMV, our approach is more efficient by several comparison experiments.

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